



ATTY. DOCKET NO. 1875.1290002

APPLICATION NO. 09/985,886

FORM 1449

APPLICANTS Seshadri et al.

## INFORMATION DISCLOSURE STATEMENT

FILING DATE November 6, 2001

GROUP 2631

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA1						
	AB1						
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## FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL1						Yes No
	AM1						Yes No
	AN1						Yes No
	AO1						Yes No
	AP1						Yes No

## OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

EG	AR	1	van Wyk <i>et al.</i> , "On the Construction of Layered Space-Time Coded Modulation (STCM) Codes Employing MTCM Code Design Techniques," <i>Proceedings of the Vehicular Technology Conference</i> , Vol.5, Conf. 50, IEEE, New York, September 19-22, 1999, pp. 2969-2973.
	AS	1	
	AT	1	

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	AL1					Yes No
	AM1					Yes No
	AN1					Yes No
	AO1					Yes No
	AP1					Yes No

## OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

EG	AR	1	S. Alamouti, "A Simple Transmit Diversity Technique for Wireless Communications," <i>IEEE Journal on Selected Areas in Communications</i> , Vol. 16, No. 8, October 1998, pp. 1451-1458.
EG	AS	1	Alamouti <i>et al.</i> , "Trellis-Coded Modulation and Transmit Diversity: Design Criteria and Performance Evaluation," <i>Proceedings of ICUPC</i> , IEEE, 1998, pp. 703-707.
EG	AT	1	Divsalar <i>et al.</i> , "Multiple Trellis Coded Modulation (MTCM)," <i>IEEE Trans. Communications</i> , Vol. 36, No., 4, April 1988, pp. 410-419.

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AL2						Yes No
AM2						Yes No
AN2						Yes No
AO2						Yes No
AP2						Yes No

## OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

Eg	AR	2	Fettweis <i>et al.</i> , "High-Rate Viterbi Processor: A Systolic Array Solution," <i>IEEE Journal on Selected Areas in Communications</i> , Vol. 8, No. 8, IEEE, October 1990, pp. 1520-1533.
Eg	AS	2	Fettweis <i>et al.</i> , "Parallel Viterbi Algorithm Implementation: Breaking the ACS-Bottleneck," <i>IEEE Transactions on Communications</i> , Vol. 37, No. 8, IEEE, August 1989, pp. 785-789.
Eg	AT	2	Forney, G., "The Viterbi Algorithm," <i>Proceedings of the IEEE</i> , Vol. 61, No. 3, March 1973, IEEE, New York, New York, pp. 268-278.

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	AC3					
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	AL3					Yes No
	AM3					Yes No
	AN3					Yes No
	AO3					Yes No
	AP3					Yes No

## OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

EG	AR	3	H. Jafarkhani, "A Quasi-Orthogonal Space-Time Block Code," <i>IEEE Trans. Communications</i> , Vol. 49, No.1, January 2001, pp. 1-4.
EG	AS	3	Siwamogsatham et al., "Robust Space-Time Coding for Correlated Rayleigh Fading Channels," <i>Proceedings - Thirty-Eighth Annual Allerton Conference on Communication, Control, and Computing - Volume II</i> , University of Illinois, October 2000, pp. 1057-1066.
EG	AT	3	G. Ungerboeck, "Channel Coding for Multilevel/Phase Signals," <i>IEEE Trans. Inform. Theory</i> , Vol. IT-28, No. 1, IEEE, New York, New York, January 1982, pp. 55-67.

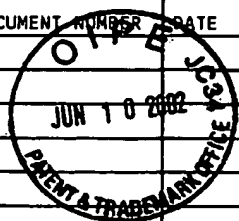
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	AL4						Yes No
	AM4						Yes No
	AN4						Yes No
	AO4						Yes No
	AP4						Yes No

## OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

Ex	AR	4	Tarokh et al., "New Detection Schemes for Transmit Diversity with no Channel Estimation," <i>IEEE 1998 International Conference on Universal Personal Communications</i> , IEEE, Piscataway, New Jersey, 1998, pp. 917-920.
Ex	AS	4	Tarokh et al., "Space-time Block Codes from Orthogonal Designs," <i>IEEE Trans. Inform. Theory</i> , Vol. 45, No. 5, IEEE, July 1999, pp. 1456-1467.
Ex	AT	4	Tarokh et al., "Space-Time Block Coding for Wireless Communications: Performance Results," <i>IEEE Journal on Selected Areas in Communications</i> , Vol. 17, No. 3, IEEE, March 1999, pp. 451-460.

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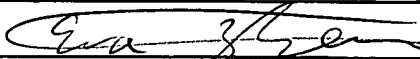
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	AL5						Yes No
	AM5						Yes No
	AN5						Yes No
	AO5						Yes No
	AP5						Yes No

## OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

Eg	AR	5	Tarokh et al., "Space-Time Codes for High Data Rate Wireless Communication: Performance Criterion and Code Construction," <i>IEEE Trans. Inform. Theory</i> , Vol. 44, No. 2, IEEE, March 1998, pp. 744-765.
	AS	5	
	AT	5	

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